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Amy Jonsson
Amy Jonsson

ATTORNEY DOCKET: RBUS 1305-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Inventor:

Jade M. Kizer

Application No. **10/645,201**

Confirmation No. **7115**

Filed: **21 August 2003**

Title: **Periodic Interface Calibration for High Speed Communication**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Group Art Unit: 2816

Examiner: *Unknown*

CUSTOMER NO. 38342

FOURTH INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §1.56

Sir:

It is requested that the information identified in this statement be considered by the Examiner and made of record in the above-identified application. This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56.

Enclosed with this statement is Form PTO-SB/08A and PTO-SB/08B. The Examiner is requested to initial the forms and return them to the undersigned in accordance with M.P.E.P. 609.

Also enclosed with this statement is a copy of each cited document as required by 37 C.F.R. 1.98. The exception to this, in accordance with the 05 August 2003 Official Gazette Notice in which the USPTO waives the requirement for submitting copies of U.S. Patent and Publications for cases filed after 30 June 2003, is that copies of U.S. Patent documents and copies of U.S. Patent publications are not being submitted.

This statement should be considered under 37 C.F.R. 1.97(b) because it is being filed before the mailing date of the first Office Action on the merits.

Fee Authorization. The Commissioner is hereby authorized to charge underpayment of any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0869 (RBUS 1305-1). A copy of this authorization is enclosed.

Respectfully submitted,

Date: 16 July 04

By: Mark A. Haynes

Mark A. Haynes, Reg. No. 30,846

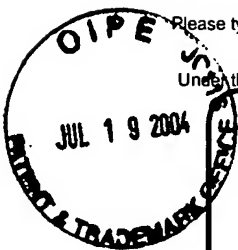
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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 4

Complete if Known

Application Number	10/645,201
Filing Date	21 August 2003
First Named Inventor	Jade M. Kizer
Group Art Unit	2816
Examiner Name	Unassigned
Attorney Docket Number	RBUS 1305-1

U.S. PATENT DOCUMENTS

Examiner Initials [*]	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	A1	3,155,102		Niederer, Jr. et al.	11-03-1964	
	A2	3,638,121		Spilker, Jr.	01-25-1972	
	A3	3,922,491		Bjork et al.	11-25-1975	
	A4	4,648,133		Vilnrotter	03-03-1987	
	A5	5,243,626		Devon et al.	09-07-1993	
	A6	5,485,490		Leung et al.	01-16-1996	
	A7	5,511,091		Saito	04-23-1996	
	A8	5,523,760		McEwan	06-04-1996	
	A9	5,554,945		Lee et al.	09-10-1996	
	A10	5,621,913		Tuttle et al.	04-15-1997	
	A11	5,742,798		Goldrian	04-21-1998	
	A12	6,369,652		Nguyen et al.	04-09-2002	
	A13	6,421,389		Jett et al.	07-16-2002	
	A14	6,606,350		Dress, Jr. et al.	08-12-2003	
	A15	6,657,468		Best et al.	12-02-2003	
	A16	6,690,741		Larrick, Jr. et al.	02-10-2004	
	A17	6,717,992		Cowie et al.	04-06-2004	
	A18	2001/0048382		Low et al.	12-06-2001	
	A19	2001/0053175		Hector et al.	12-20-2001	
	A20	2003/0063597		Suzuki	04-03-2003	

FOREIGN PATENT DOCUMENTS

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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
	B1	JP	02000035831A		Nitsusan Jidosha KK	08-14-1990	Abstract only	
	B2							
	B3							
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	B5							
	B6							
	B7							
	B8							
	B9							
	B10							

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Date
Considered

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Application Number		10/645,201			
Filing Date		21 August 2003			
First Named Inventor		Jade M. Kizer			
Group Art Unit		2816			
Examiner Name		Unassigned			
Attorney Docket Number		RBUS 1305-1			
Sheet	2	of	4		

U.S. PATENT DOCUMENTS						
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		Number	Kind Code ² (if known)			
	A21	2003/0146800		Dvorak	08-07-2003	
	A22	2003/0198212		Hector et al.	10-23-2003	
	A23	2003/0198308		Hector et al.	10-23-2003	
	A24	2004/0032354		Knobel et al.	02-19-2004	
	A25	2004/0057500		Balachandran et al.	03-25-2004	
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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
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	B12							
	B13							
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	B20							

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			Application Number	10/645,201
			Filing Date	21 August 2003
			First Named Inventor	Jade M. Kizer
			Group Art Unit	2816
			Examiner Name	Unassigned
			Attorney Docket Number	RBUS 1305-1
Sheet	3	of	4	

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	C1	ZERBE, JARED ET AL., U.S. Patent Application No. 09/776,550 filed 2 February 2001, entitled "Method and Apparatus for Evaluating and Calibrating a Signaling System."	
	C2	ZERBE, JARED ET AL, U.S. Patent Application No. 09/976,170 filed 21 October 2001, entitled "Method and Apparatus for Evaluating and Optimizing a Signaling System."	
	C3	U.S. Patent Application No. 10/278,708 filed 22 October 2002, entitled "Phase Adjustment Apparatus and Method for a Memory Device Signaling System."	
	C4	DANIELE, N. et al., "Principle and Motivations of UWB Technology for High Data Rate WPAN Applications," SOC 2003, 4 pages.	
	C5	WIN, MOE Z., et al., "Impulse Radio: How it Works," IEEE Communications Letters 2(2), Feb. 1998, 36-38.	
	C6	DANIELE, NORBERT, "Ultra Wide Band Principles and Applications for Wireless Communications," CEA-LETI Annual Review, 25 and 26 June 2002, 24 pages.	
	C7	U.S. Patent Application No. 09/941,079 filed 28 August 2001 entitled "Clock Data Recovery with Selectable Phase Control Input."	
	C8	Yang, Chih-Kong Ken "Design of High-Speed Serial Links in CMOS" Technical Report No. CSL-TR-98-775, Sponsored By Center for Integrated Systems, Sun Microsystems and LSI Logic Inc. December 1998, pp. 1-182.	
	C9	NAKASE, YASUNOBU ET AL, "Source-Synchronization and Timing Vernier Techniques for 1.2-GB/s SDRAM Interface," IEEE Journal of Solid-State Circuits, Vol. 34, No. 4 (Apr. 1999), 494-501.	
	C10	GILLINGHAM, PETER, "SLDRAM Architectural and Functional Overview," SLDRAM Consortium (29 Aug 1997), 1-14.	
	C11	"Draft Standard for a High-Speed Memory Interface (SyncLink)", Draft 0.99 IEEE P1596.7-199X (1996), 56 pages	

Examiner Signature	Date Considered
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<div style="text-align: right;">Substitute for form 1449B/PTO</div> <h2 style="margin: 0;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="margin: 5px 0 0 40px;"><i>(use as many sheets as necessary)</i></p>			Complete if Known		
Sheet	4	of	4	Application Number	10/645,201
				Filing Date	21 August 2003
				First Named Inventor	Jade M. Kizer
				Group Art Unit	2816
				Examiner Name	Unassigned
				Attorney Docket Number	RBUS 1305-1

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	C12	GILLINGHAM, PETER, "SLDRAM: High-Performance, Open-Standard Memory," IEEE (1997), 29-39	
	C13	SLDRAM Inc., "400 Mb/s/pin SLDRAM," Draft Advance (9 Jul 1998), 1-69.	
	C14	RAMBUS "Direct Rambus Short Channel Layout Guide, Version 0.95" (August 2001), 40 pages	
	C15	RAMBUS "Direct Rambus Long Channel Design Guide" (2000), 45 pages	
	C16	INTEL "How to Measure RDRAM* System Clock Jitter" Application Note AP-667 (June 1999), pp. 1-15	
	C17	CHANG, KUN YUNG "Design of a CMOS Asymmetric Serial Link" A Dissertation Submitted to the Department of Electrical Engineering and the Committee on Graduate Studies of Stanford University August 1999, 119 pages	
	C18	RAMBUS, INC. "RDRAM Direct Rambus Clock Generator" (April 2002), 22 pages	
	C19		
	C20		
	C21		

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